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PATENT  
30090084-5 US (1509-236A)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin LLOYD-JONES                      Conf.: 6585  
Serial No.: 10/734,624                              Art Unit: 2187  
Filed: December 15, 2003                          Examiner: C. Chace  
For: ADDRESS MAPPING IN SOLID STATE STORAGE DEVICE

INFORMATION DISCLOSURE STATEMENT  
AND REQUEST FOR FORM PTO-892

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

February 8, 2005

Sir:

In accordance with the provisions of 37 C.F.R. §1.56, §1.97, and §1.98, the Examiner's attention is hereby directed to the documents listed on the attached Form PTO-1449. The listed references were cited during prosecution of parent Application Serial No. 09/959,590, filed December 21, 2001. It is respectfully requested that the listed documents be expressly considered during prosecution of this continuation application, and that the documents be made of record herein and appear among the "References Cited" on any patent to issue from the present application.

Authorization for payment of the \$180 fee is attached. If in error, the Commissioner is hereby authorized to charge any prescribed fees not otherwise provided for, including application

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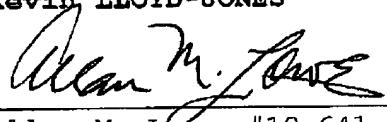
processing, extension of time, extra claims, and IDS fees, to  
Deposit Account No. 08-2025.

In addition, no Form PTO-892 was included with the Office  
Action mailed August 30, 2004, although two references were cited  
in the body of the Office Action. Accordingly, the Examiner is  
courteously requested to provide a Form PTO-892 with the next  
official communication.

Respectfully submitted,

Kevin LLOYD-JONES

By:

  
Allan M. Lowe, #19,641

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Sheet 1 of 1

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(FORM PTO-1449)</b>				DOCKET NO. 1509-236A		SERIAL NO. 10/734,624	
				APPLICANT <b>Kevin LLOYD-JONES</b>			
				FILED December 15, 2003		ART UNIT 2187	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	5,638,334	6-10-97	FARMWALD et al.				
	5,359,570	10-25-94	HSU et al.				
	6,445,613	9-3-02	NAGAI				
	5,404,485	4-4-95	BAN				
	5,749,091	5-5-98	ISHIDA et al.				
	6,115,785	9-5-00	ESTAKHRI et al.				
	6,212,647	4-3-01	SIMS III et al.				
	2001/0002488	5-31-01	SIMS et al.				
	6,249,884	6-19-01	JOO				
	6,345,368	2-5-02	BERGSTEN				
	6,393,513	5-21-02	ESTAKHRI et al.				
	6,470,461	10-22-02	PINVIDIC et al.				
	6,477,616	11-5-02	TAKAHASHI				
	2001/0017798	8-30-01	ISHII				
	20010014039	8-16-01	LONGWELL et al.				
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes No	
	99 38066 A	07/29/99	WIPO				
OTHER ART (including Author, Title, Date, Patent No. Pages, Etc.)							
	SAKALAY, "Address skip - a means for utilizing imperfect storage," <i>IBM Technical Disclosure Bulletin</i> , pp. 1841-1842, October 1, 1977, Vol. 20, No. 5.						
EXAMINER				DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

AML:alw:rk